TL071C,AC
TL072C,AC
TL074C,AC

Low Noise, JFET Input Operational Amplifiers

These low noise JFET input operational amplifiers combine two state–of–the–art analog technologies on a single monolithic integrated circuit. Each internally compensated operational amplifier has well matched high voltage JFET input device for low input offset voltage. The BIFET technology provides wide bandwidths and fast slew rates with low input bias currents, input offset currents, and supply currents. Moreover, the devices exhibit low noise and low harmonic distortion, making them ideal for use in high fidelity audio amplifier applications.

These devices are available in single, dual and quad operational amplifiers which are pin–compatible with the industry standard MC1741, MC1458, and the MC3403/LM324 bipolar products.

- Low Input Noise Voltage: 18 nV/$\sqrt{Hz}$ Typ
- Low Harmonic Distortion: 0.01% Typ
- Low Input Bias and Offset Currents
- High Input Impedance: 10^{12} Ω Typ
- High Slew Rate: 13 V/μs Typ
- Wide Gain Bandwidth: 4.0 MHz Typ
- Low Supply Current: 1.4 mA per Amp

LOW NOISE, JFET INPUT OPERATIONAL AMPLIFIERS

SEMICONDUCTOR TECHNICAL DATA

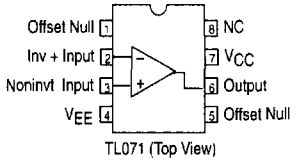


P SUFFIX
PLASTIC PACKAGE
CASE 626

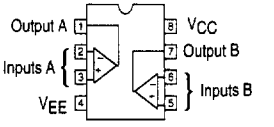


D SUFFIX
PLASTIC PACKAGE
CASE 751
(SO–8)

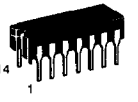
PIN CONNECTIONS



TL071 (Top View)

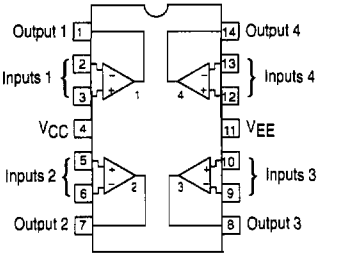


TL072 (Top View)



N SUFFIX
PLASTIC PACKAGE
CASE 646
(TL074 Only)

PIN CONNECTIONS



TL074 (Top View)

ORDERING INFORMATION

Op Amp Function	Device	Operating Temperature Range	Package
Single	TL071ACD, CD	T_A = 0° to +70°C	SO–8
	TL071ACP, CP		Plastic DIP
Dual	TL072ACD, CD	T_A = 0° to +70°C	SO–8
	TL072ACP, CP		Plastic DIP
Quad	TL074ACN, CN	T_A = 0° to +70°C	Plastic DIP

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MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Supply Voltage	V_{CC} V_{EE}	+18 −18	V
Differential Input Voltage	V_{ID}	±30	V
Input Voltage Range (Note 1)	V_{IDR}	±15	V
Output Short Circuit Duration (Note 2)	t_{SC}	Continuous	
Power Dissipation Plastic Package (N, P) Derate above T_A = +47°C	P_D $1/\theta_{JA}$	680 10	mW mW/°C
Operating Ambient Temperature Range	T_A	0 to +70	°C
Storage Temperature Range	T_{stg}	−65 to +150	°C

NOTES: 1. The magnitude of the input voltage must not exceed the magnitude of the supply voltage or 15 V, whichever is less.
2. The output may be shorted to ground or either supply. Temperature and/or supply voltages must be limited to ensure that power dissipation ratings are not exceeded.

ELECTRICAL CHARACTERISTICS (V_{CC} = +15 V, V_{EE} = −15 V, T_A = T_{high} to T_{low} [Note 3])

Characteristics	Symbol	Min	Typ	Max	Unit
Input Offset Voltage ($R_S \le 10$ k, V_{CM} = 0) TL071C, TL072C TL074C TL07_AC	V_{IO}	 — — —	 — — —	 13 13 7.5	mV
Input Offset Current (V_{CM} = 0) (Note 4) TL07_C TL07_AC	I_{IO}	 — —	 — —	 2.0 2.0	nA
Input Bias Current (V_{CM} = 0) (Note 4) TL07_C TL07_AC	I_{IB}	 — —	 — —	 7.0 7.0	nA
Large–Signal Voltage Gain (V_O = ±10 V, $R_L \ge 2.0$ k) TL07_C TL07_AC	A_{VOL}	 15 25	 — —	 — —	V/mV
Output Voltage Swing (Peak–to–Peak) ($R_L \ge 10$ k) ($R_L \ge 2.0$ k)	V_O	 24 20	 — —	 — —	V

NOTES: 3. T_{low} = 0°C for TL071C,AC, TL072C,AC, TL074C,AC; T_{high} = +70°C for TL071C,AC, TL072C,AC, TL074C,AC
4. Input Bias currents of JFET input op amps approximately double for every 10°C rise in junction temperature as shown in Figure 3. To maintain junction temperature as close to ambient temperature as possible, pulse techniques must be used during testing.

Figure 1. Unity Gain Voltage Follower

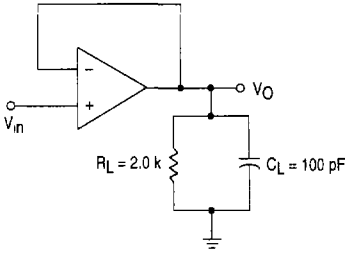
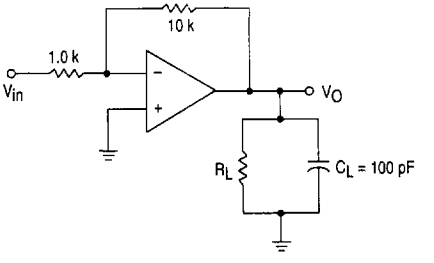


Figure 2. Inverting Gain of 10 Amplifier



ELECTRICAL CHARACTERISTICS (V_{CC} = +15 V, V_{EE} = −15 V, T_A = 25°C, unless otherwise noted.)

Characteristics	Symbol	Min	Typ	Max	Unit
Input Offset Voltage ($R_S \le 10$ k, V_{CM} = 0) TL071C, TL072C TL074C TL07_AC	V_{IO}	 — — —	 3.0 3.0 3.0	 10 10 6.0	mV
Average Temperature Coefficient of Input Offset Voltage R_S = 50 Ω, $T_A = T_{low}$ to T_{high} (Note 3)	$\Delta V_{IO}/\Delta T$	—	10	—	μV/°C
Input Offset Current (V_{CM} = 0) (Note 4) TL07_C TL07_AC	I_{IO}	 — —	 5.0 5.0	 50 50	pA
Input Bias Current (V_{CM} = 0) (Note 4) TL07_C TL07_AC	I_{IB}	 — —	 30 30	 200 200	pA
Input Resistance	r_i	—	10^{12}	—	Ω
Common Mode Input Voltage Range TL07_C TL07_AC	V_{ICR}	 ±10 ±11	 +15, −12 +15, −12	 — —	V
Large–Signal Voltage Gain (V_O = ±10 V, $R_L \ge$ 2.0 k) TL07_C TL07_AC	A_{VOL}	 25 50	 150 150	 — —	V/mV
Output Voltage Swing (Peak–to–Peak) (R_L = 10 k)	V_O	24	28	—	V
Common Mode Rejection Ratio ($R_S \le$ 10 k) TL07_C TL07_AC	CMRR	 70 80	 100 100	 — —	dB
Supply Voltage Rejection Ratio ($R_S \le$ 10 k) TL07_C TL07_AC	PSRR	 70 80	 100 100	 — —	dB
Supply Current (Each Amplifier)	I_D	—	1.4	2.5	mA
Unity Gain Bandwidth	BW	—	4.0	—	MHz
Slew Rate (See Figure 1) V_{in} = 10 V, R_L = 2.0 k, C_L = 100 pF	SR	—	13	—	V/μs
Rise Time (See Figure 1)	t_r	—	0.1	—	μs
Overshoot (V_{in} = 20 mV, R_L = 2.0 k, C_L = 100 pF)	OS	—	10	—	%
Equivalent Input Noise Voltage R_S = 100 Ω, f = 1000 Hz	e_n	—	18	—	nV/$\sqrt{Hz}$
Equivalent Input Noise Current R_S = 100 Ω, f = 1000 Hz	i_n	—	0.01	—	pA/$\sqrt{Hz}$
Total Harmonic Distortion V_O (RMS) = 10 V, $R_S \le$ 1.0 k, $R_L \ge$ 2.0 k, f = 1000 Hz	THD	—	0.01	—	%
Channel Separation A_V = 100	CS	—	120	—	dB

NOTES: 3. T_{low} = 0°C for TL071C,AC, TL072C,AC, TL074C,AC; T_{high} = +70°C for TL071C,AC, TL072C,AC, TL074C,AC

4. Input Bias currents of JFET input op amps approximately double for every 10°C rise in junction temperature as shown in Figure 3. To maintain junction temperature as close to ambient temperature as possible, pulse techniques must be used during testing.

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Figure 3. Input Bias Current versus Temperature

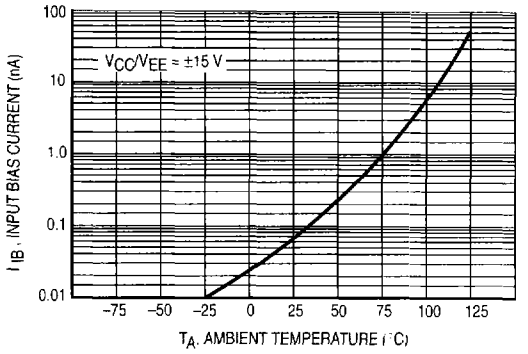


Figure 4. Output Voltage Swing versus Frequency

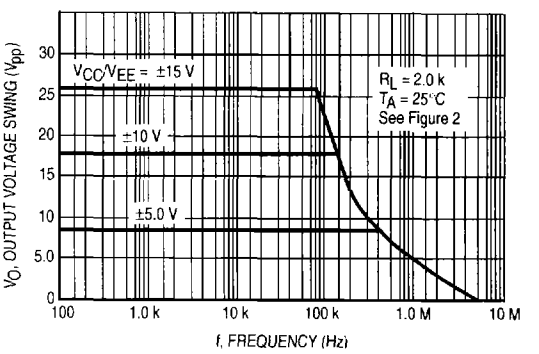


Figure 5. Output Voltage Swing versus Load Resistance

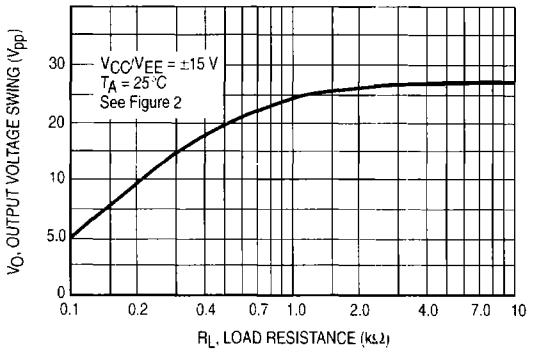


Figure 6. Output Voltage Swing versus Supply Voltage

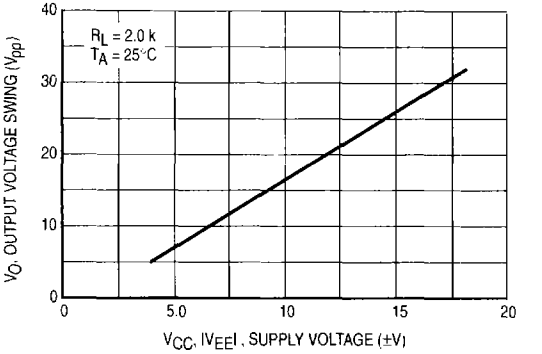


Figure 7. Output Voltage Swing versus Temperature

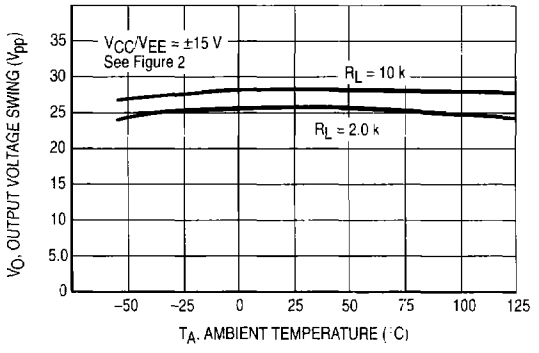


Figure 8. Supply Current per Amplifier versus Temperature

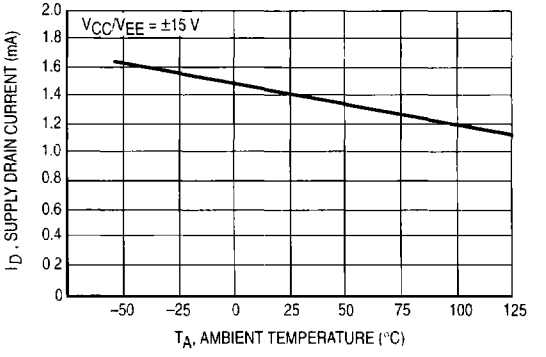


Figure 9. Large Signal Voltage Gain and Phase Shift versus Frequency

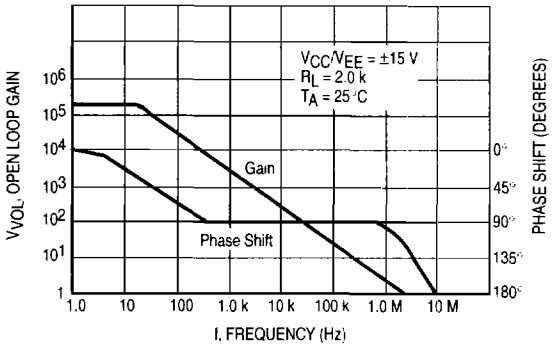


Figure 10. Large Signal Voltage Gain versus Temperature

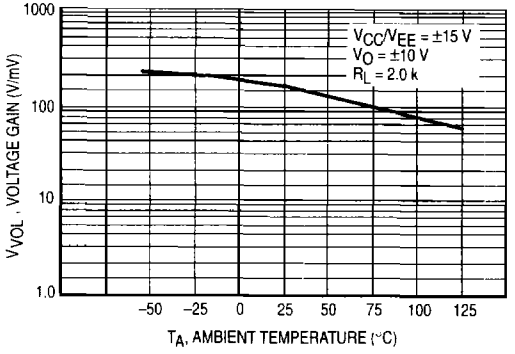


Figure 11. Normalized Slew Rate versus Temperature

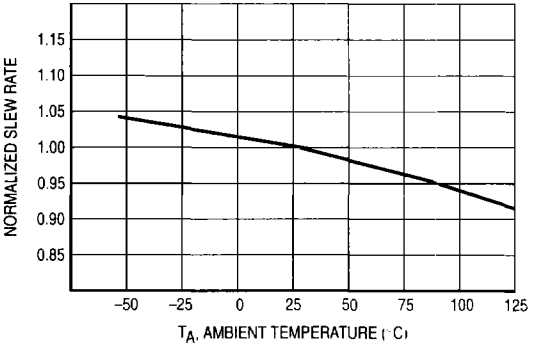


Figure 12. Equivalent Input Noise Voltage versus Frequency

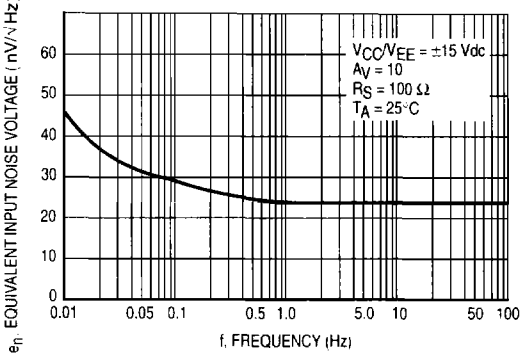
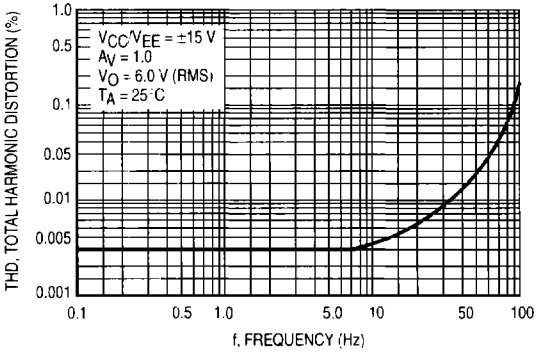


Figure 13. Total Harmonic Distortion versus Frequency



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Representative Schematic Diagram
(Each Amplifier)

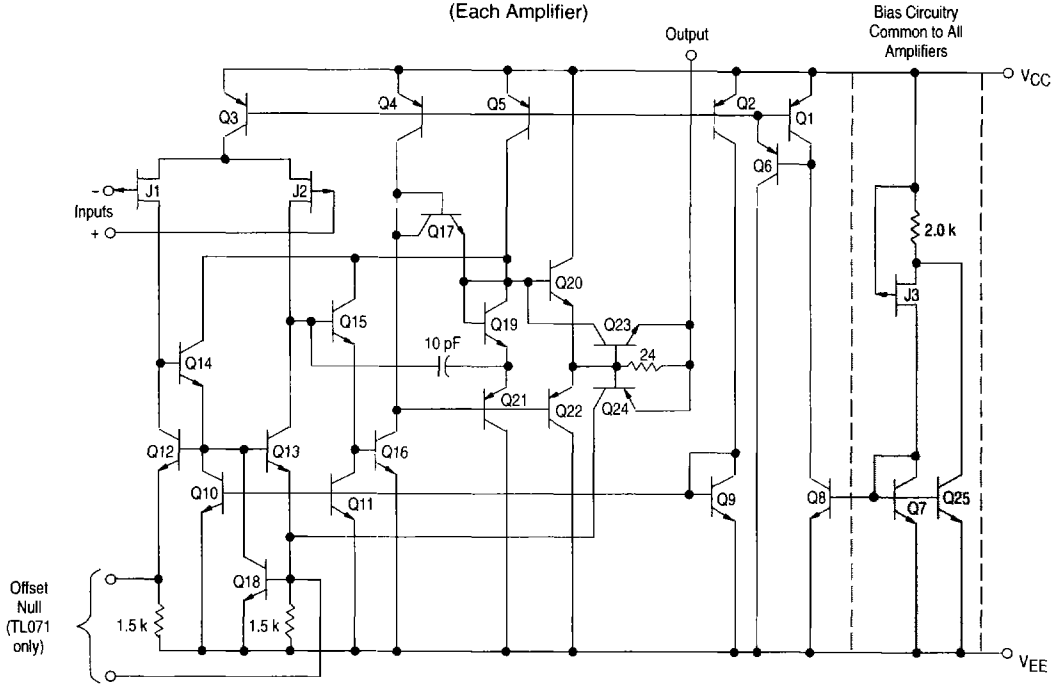

Figure 14. Audio Tone Control Amplifier

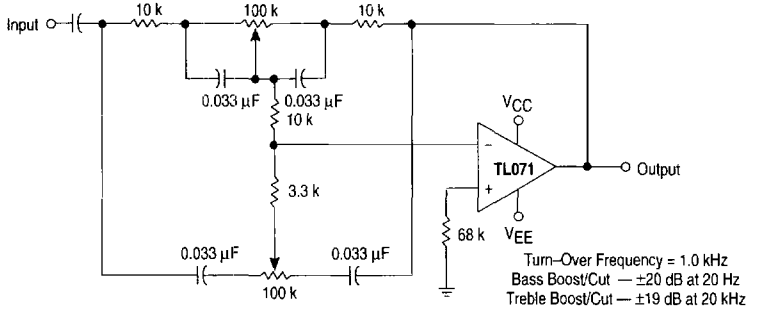

Figure 15. High Q Notch Filter

